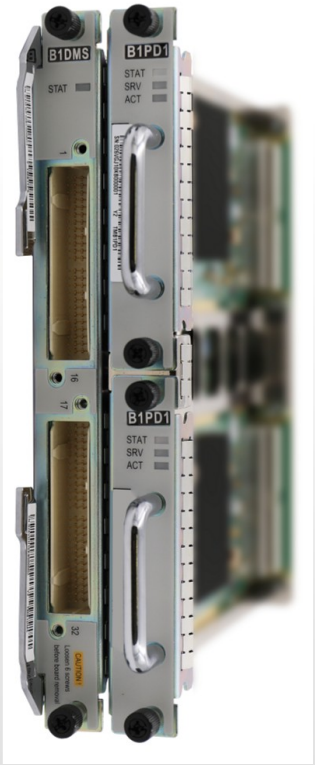
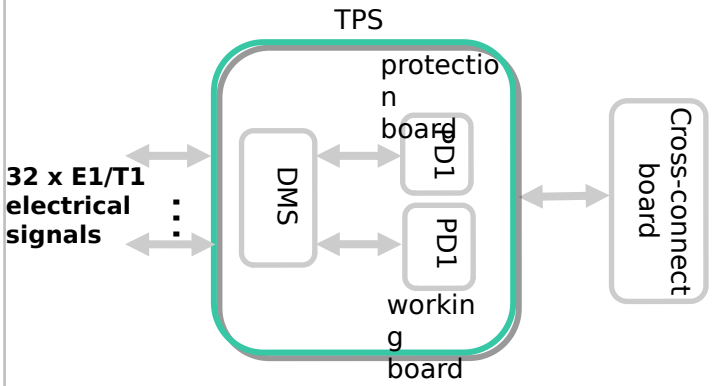
TMB1TPS

Initial Version:V100R019C10

Receives/Transmits 32xE1/T1 signals. When two PD1 boards are used together with the DMS interface board, TPS protection can be provided.



Service



NOTE: Two PD1 boards and one DMS board work together to implement the TPS protection function.

Function

Function	Description
Alarms and performance events	Monitors the operating temperature of the board and reports alarms.
TPS protection	Two PD1 boards and one DMS board work together to provide TPS protection.
Tributary clock sources	Supports source selection for E1 and T1 clocks.

Slot

Dual-slot board	
E6608	Slot1 and Slot3, Slot3 and Slot5, Slot2 and Slot4, Slot4 and Slot6
E6616	Slot1 and Slot3, Slot2 and Slot4, Slot3 and Slot5, Slot4 and Slot6, Slot7 and Slot9, Slot8 and Slot10, Slot9 and Slot11, Slot10 and Slot12, Slot11 and Slot13, Slot12 and Slot14

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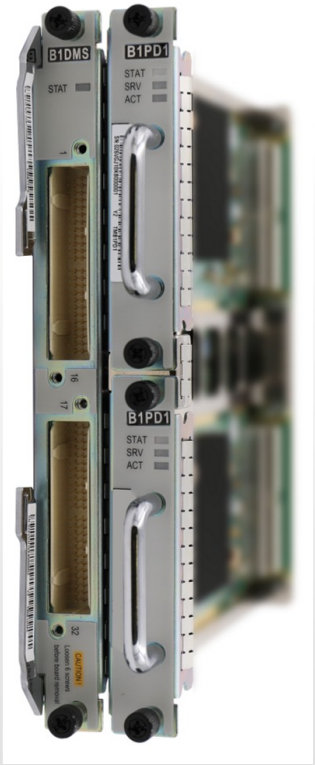

Specifications of the E1/T1 electrical ports

Item	Value	
Electrical Port Type	1544 kbit/s	2048 kbit/s
Code pattern	B8ZS code, AMI code	HDB3 code
Waveform at the output port	Complies with ITU-T G.703.	Complies with ITU-T G.703.
Signal bit rate at the output port		
Allowed attenuation at the input port		
Permitted frequency deviation at the input port	Complies with ITU-T G.823.	
Input jitter tolerance	Complies with ITU-T G.824.	Complies with ITU-T G.823.
Anti-interference capability at the input port	-	Complies with ITU-T G.703.
Reflection attenuation at the input and output ports	-	
Output jitter	Complies with ITU-T G.823 and G.824.	
Mapping jitter	-	Complies with ITU-T G.783.
Combined jitter	-	
Jitter transfer function	-	Complies with ITU-T G.742.
Port type	Anea 96	